

**ECR #: P7**

**Tracker #: 6**

**Status: Ratified**

**Title: Revise the Revision History**

**Release Date: September 17, 1997**

**Impact, Low: Clarification**

**Spec Version: NLX Motherboard Specification 1.2**

**Summary:**

Correct errors in the Revision History for references to figures that were added or modified from the Revision 1.1 to 1.2 of the specification.

**Background:**

Some figure numbers were changed during the final stages of editing, and the Revision History was not updated to reflect those changes. Some text was added to the Revision History to accurately reflect the titles of the figures. Only the underlined portions below are changed.

**Change Current Specification As Shown:**

In the Revision History of the NLX 1.2 specification, correct the following entries as shown.

**Revision History**

**Revision 1.1 to 1.2 (detailed)**

- Figure 3.9 (was incorrectly listed as Figure 3.1): NLX Card Edge Detail, Primary (Top) Side - Changed board width tolerance to +0.007, -0.005. Dimensioned cutout in front of 340 pin connector. Included 2.00mm reference dimension from gold finger centerline, to notch edge.
- Figure 3.12.1 (was incorrectly listed as Figure 3.10.1): Reserved Space for Workstation/Server Performance Connector - Added this figure.
- Figure 3.14 (was incorrectly listed as Figure 3.12): A.G.P. Connector pin 1 Location - Added A.G.P. connector location detail.
- Figure 3.15 (was incorrectly listed as Figure 3.13): NLX Motherboard Primary Side Height Restrictions - Changed keepout dimension from 4.700 inches to 4.750 inches. Changed second keepout dimension from 7.8 inches to 7.46 inches. Other figure changes are consistent with general figure changes noted above.
- Figure 3.17 (was incorrectly listed as Figure 3.15): Chassis Back Panel I/O Shield Opening - View changed to be viewed from outside the system.
- Figure 3.18 (was incorrectly listed as Figure 3.16): Side View of Back Panel Double High I/O Shield Opening Dimensions - Added shield clearance detail, added 1.690 inches dimension for double high opening, added 0.300 inches dimension for motherboard to chassis gap.
- Figure 3.19 (was incorrectly listed as Figure 3.17): Side View of Single High I/O Shield Opening Dimensions - Added this figure.
- Figure 3.20 (was incorrectly listed as Figure 3.18): Side View of Double Stack I/O Shield Opening Dimensions - Added this figure.
- Section 3.7, A.G.P. Support: In the fourth text paragraph, the reference to Figure 3.12 should be to Figure 3.14.